Introduction to quantum algorithms

Daniel J. Bernstein University of Illinois at Chicago & Technische Universiteit Eindhoven Data ("state") stored in n bits: an element of $\{0, 1\}^n$, often viewed as representing an element of $\{0, 1, ..., 2^n - 1\}$. Introduction to quantum algorithms

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 $(a_1, a_0, a_3, a_2, a_5, a_4, a_7, a_6)$ is measured as $(q_0 \oplus 1, q_1, q_2)$, representing $q \oplus 1$, with probability $|a_q|^2 / \sum_r |a_r|^2$.

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3-qubit states:

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$(a_0, a_1, a_2, a_3, a_4, a_1)$ $(a_4, a_5, a_6, a_7, a_0, a_1)$ is "complementing $(q_0, q_1, q_2) \mapsto (q_0, q_1)$

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Fast quantum operations, part 1

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Complementing qubit 2

- = swapping qubits 0 and 2
 - complementing qubit 0
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Similarly: swapping qubits *i*, *j*.

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 $a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $a_3, a_2, a_5, a_4, a_7, a_6)$ ementing index bit 0, complementing qubit 0".

 $a_2, a_3, a_4, a_5, a_6, a_7)$ ared as (q_0, q_1, q_2) , ting $q = q_0 + 2q_1 + 4q_2$, bability $|a_q|^2 / \sum_r |a_r|^2$.

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 $a_5, a_6, a_7) \mapsto$ $a_4, a_7, a_6)$ index bit 0, nting qubit 0".

 $a_{5}, a_{6}, a_{7})$ $a_{1}, q_{2}),$ $a_{0} + 2q_{1} + 4q_{2},$ $a_{q}|^{2} / \sum_{r} |a_{r}|^{2}.$ $a_{4}, a_{7}, a_{6})$ $\oplus 1, q_{1}, q_{2}),$ $a_{1}|^{2} / \sum_{r} |a_{r}|^{2}.$

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(*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₅ (*a*₀, *a*₁, *a*₃, *a*₂, *a*₄, *a*₄, *a*₄, *a*₁, *a*₁, *a*₁, *a*₁, *a*₂, *a*₁, *a*₁, *a*₂, *a*₁, *a*₁ is a "reversible XC "controlled NOT $(q_0,q_1,q_2)\mapsto (q_0)$ Example with mor (*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₅ *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂ *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a* \mapsto (a_0 , a_1 , a_3 , a_2 , a_3 *a*₈, *a*₉, *a*₁₁, *a*₁₀, *a*₁₂ *a*₁₆, *a*₁₇, *a*₁₉, *a*₁₈, *a a*₂₄, *a*₂₅, *a*₂₇, *a*₂₆, *a*

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 \circ swapping qubits 0 and 2.

Similarly: swapping qubits *i*, *j*.

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7)$ $(a_0, a_1, a_3, a_2, a_4, a_5, a_7, a_6)$ is a "reversible XOR gate" = "controlled NOT gate": $(q_0,q_1,q_2)\mapsto (q_0\oplus q_1,q_1,q_1,q_1)$ Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂ a24, a25, a26, a27, a28, a29, a30 \mapsto (a_0 , a_1 , a_3 , a_2 , a_4 , a_5 , a_7 , a_8 *a*₈, *a*₉, *a*₁₁, *a*₁₀, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₉, *a*₁₈, *a*₂₀, *a*₂₁, *a*₂₃ *a*₂₄, *a*₂₅, *a*₂₇, *a*₂₆, *a*₂₈, *a*₂₉, *a*₃₅ $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_4, a_5, a_6, a_7, a_0, a_1, a_2, a_3)$ is "complementing qubit 2": $(q_0, q_1, q_2) \mapsto (q_0, q_1, q_2 \oplus 1).$

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_4, a_2, a_6, a_1, a_5, a_3, a_7)$ is "swapping qubits 0 and 2": $(q_0, q_1, q_2) \mapsto (q_2, q_1, q_0).$

Complementing qubit 2 = swapping qubits 0 and 2 • complementing qubit 0 • swapping qubits 0 and 2.

Similarly: swapping qubits *i*, *j*.

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_3, a_2, a_4, a_5, a_7, a_6)$ is a "reversible XOR gate" ="controlled NOT gate": $(q_0, q_1, q_2) \mapsto (q_0 \oplus q_1, q_1, q_2).$ Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃, $a_{24}, a_{25}, a_{26}, a_{27}, a_{28}, a_{29}, a_{30}, a_{31}$ \mapsto (*a*₀, *a*₁, *a*₃, *a*₂, *a*₄, *a*₅, *a*₇, *a*₆, *a*₈, *a*₉, *a*₁₁, *a*₁₀, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₉, *a*₁₈, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, $a_{24}, a_{25}, a_{27}, a_{26}, a_{28}, a_{29}, a_{31}, a_{30}$). $a_2, a_3, a_4, a_5, a_6, a_7) \mapsto a_6, a_7, a_0, a_1, a_2, a_3)$ plementing qubit 2'': $a_2) \mapsto (q_0, q_1, q_2 \oplus 1).$

 $a_2, a_3, a_4, a_5, a_6, a_7) \mapsto a_2, a_6, a_1, a_5, a_3, a_7)$ oping qubits 0 and 2": $a_2) \mapsto (q_2, q_1, q_0).$

nenting qubit 2 oing qubits 0 and 2 oplementing qubit 0 pping qubits 0 and 2.

: swapping qubits *i*, *j*.

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_3, a_2, a_4, a_5, a_7, a_6)$ is a "reversible XOR gate" "controlled NOT gate": $(q_0,q_1,q_2)\mapsto (q_0\oplus q_1,q_1)$ Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄ *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂ *a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a*₂₈, *a*₂₉, *a*₃ \mapsto (*a*₀, *a*₁, *a*₃, *a*₂, *a*₄, *a*₅, *a*₇, *a*₈, *a*₉, *a*₁₁, *a*₁₀, *a*₁₂, *a*₁₃, *a*₁₅ *a*₁₆, *a*₁₇, *a*₁₉, *a*₁₈, *a*₂₀, *a*₂₁, *a*₂ *a*₂₄, *a*₂₅, *a*₂₇, *a*₂₆, *a*₂₈, *a*₂₉, *a*₃

$) \mapsto$
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, <i>a</i> 15,
₂₂ , a ₂₃ ,
₃₀ , a ₃₁)
<i>a</i> ₆ ,
, <i>a</i> ₁₄ ,
23, a 22,
₃₁ , a ₃₀).

 (a_0, a_1, a_1) (a_0, a_1, a_1) is a "To "control (q_0, q_1, q_1) Example (a_0, a_1, a_1) *a*₈, *a*₉, *a*₂ *a*₁₆, *a*₁₇, *a*₂₄, *a*₂₅, \mapsto (a_0 , a*a*₈, *a*₉, *a*₂ *a*₁₆, *a*₁₇, *a*₂₄, *a*₂₅,

 $a_5, a_6, a_7) \mapsto$ a₁, a₂, a₃) g qubit 2": , q_1 , $q_2 \oplus 1)$. $a_5, a_6, a_7) \mapsto$ $a_5, a_3, a_7)$ cs 0 and 2": , q_1 , q_0). ubit 2 s 0 and 2ng qubit 0 its 0 and 2. g qubits *i*, *j*.

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_3, a_2, a_4, a_5, a_7, a_6)$ is a "reversible XOR gate" = "controlled NOT gate": $(q_0,q_1,q_2)\mapsto (q_0\oplus q_1,q_1,q_2).$ Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃, *a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a*₂₈, *a*₂₉, *a*₃₀, *a*₃₁) \mapsto (*a*₀, *a*₁, *a*₃, *a*₂, *a*₄, *a*₅, *a*₇, *a*₆, *a*₈, *a*₉, *a*₁₁, *a*₁₀, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₉, *a*₁₈, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, *a*₂₄, *a*₂₅, *a*₂₇, *a*₂₆, *a*₂₈, *a*₂₉, *a*₃₁, *a*₃₀).

 $(a_0, a_1, a_2, a_3, a_4, a_6)$ (*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₄, *a*₁, *a*₂, *a*₃, *a*₄, *a*₁, *a*₁, *a*₂, *a*₃, *a*₄, *a*₁, *a*₁, *a*₂, *a*₁, *a*₁, *a*₂, *a*₁, *a*₁, *a*₁, *a*₂, *a*₁, *a*₁ is a "Toffoli gate" "controlled contro $(q_0, q_1, q_2) \mapsto (q_0)$ Example with mor (*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₅ *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂ *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a* \mapsto (a_0 , a_1 , a_2 , a_3 , a_3 *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂ *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a*

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_3, a_2, a_4, a_5, a_7, a_6)$ is a "reversible XOR gate" = "controlled NOT gate": $(q_0,q_1,q_2)\mapsto (q_0\oplus q_1,q_1,q_2).$ Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃, $a_{24}, a_{25}, a_{26}, a_{27}, a_{28}, a_{29}, a_{30}, a_{31}$ \mapsto (*a*₀, *a*₁, *a*₃, *a*₂, *a*₄, *a*₅, *a*₇, *a*₆, *a*₈, *a*₉, *a*₁₁, *a*₁₀, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₉, *a*₁₈, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, $a_{24}, a_{25}, a_{27}, a_{26}, a_{28}, a_{29}, a_{31}, a_{30}$).

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, *j*.

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7)$ $(a_0, a_1, a_2, a_3, a_4, a_5, a_7, a_6)$ is a "Toffoli gate" = "controlled controlled NOT $(q_0,q_1,q_2)\mapsto (q_0\oplus q_1q_2,q_2)$ Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂ a24, a25, a26, a27, a28, a29, a30 \mapsto ($a_0, a_1, a_2, a_3, a_4, a_5, a_7, a_8$ *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₃ *a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a*₂₈, *a*₂₉, *a*₃₅

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_3, a_2, a_4, a_5, a_7, a_6)$ is a "reversible XOR gate" = "controlled NOT gate": $(q_0, q_1, q_2) \mapsto (q_0 \oplus q_1, q_1, q_2).$

Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃, *a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a*₂₈, *a*₂₉, *a*₃₀, *a*₃₁) \mapsto (*a*₀, *a*₁, *a*₃, *a*₂, *a*₄, *a*₅, *a*₇, *a*₆, *a*₈, *a*₉, *a*₁₁, *a*₁₀, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₉, *a*₁₈, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, *a*₂₄, *a*₂₅, *a*₂₇, *a*₂₆, *a*₂₈, *a*₂₉, *a*₃₁, *a*₃₀).

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_2, a_3, a_4, a_5, a_7, a_6)$ is a "Toffoli gate" = $(q_0, q_1, q_2) \mapsto (q_0 \oplus q_1q_2, q_1, q_2).$ Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃, *a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a*₂₈, *a*₂₉, *a*₃₀, *a*₃₁) \mapsto (*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₅, *a*₇, *a*₆, *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, *a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a*₂₈, *a*₂₉, *a*₃₁, *a*₃₀).

- "controlled controlled NOT gate":

 $a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ a₃, a₂, a₄, a₅, a₇, a₆) ersible XOR gate" = led NOT gate": $(q_2)\mapsto (q_0\oplus q_1,q_1,q_2).$

e with more qubits:

*a*₂, *a*₃, *a*₄, *a*₅, *a*₆, *a*₇,

10, *a*11, *a*12, *a*13, *a*14, *a*15,

*a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃, $a_{26}, a_{27}, a_{28}, a_{29}, a_{30}, a_{31})$

1, *a*₃, *a*₂, *a*₄, *a*₅, *a*₇, *a*₆,

1, *a*10, *a*12, *a*13, *a*15, *a*14,

*a*₁₉, *a*₁₈, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, *a*₂₇, *a*₂₆, *a*₂₈, *a*₂₉, *a*₃₁, *a*₃₀).

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_2, a_3, a_4, a_5, a_7, a_6)$ is a "Toffoli gate" = "controlled controlled NOT gate": $(q_0, q_1, q_2) \mapsto (q_0 \oplus q_1q_2, q_1, q_2).$ Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃, $a_{24}, a_{25}, a_{26}, a_{27}, a_{28}, a_{29}, a_{30}, a_{31}$ \mapsto (*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₅, *a*₇, *a*₆, *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, *a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a*₂₈, *a*₂₉, *a*₃₁, *a*₃₀).

Reversib

Say p is of {0, 1,

General these fas

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 $(a_{p(0)}, a_{p(0)})$ \mapsto (a_0 , a $a_5, a_6, a_7) \mapsto$ $a_5, a_7, a_6)$ $\mathsf{PR} \mathsf{gate}'' =$ gate": $\oplus q_1, q_1, q_2).$ e qubits: *a*₅, *a*₆, *a*₇, , *a*₁₃, *a*₁₄, *a*₁₅, ₂₀, *a*₂₁, *a*₂₂, *a*₂₃, ₂₈, *a*₂₉, *a*₃₀, *a*₃₁) 4, *a*₅, *a*₇, *a*₆, , *a*₁₃, *a*₁₅, *a*₁₄, ₂₀, *a*₂₁, *a*₂₃, *a*₂₂, ₂₈, *a*₂₉, *a*₃₁, *a*₃₀).

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_2, a_3, a_4, a_5, a_7, a_6)$ is a "Toffoli gate" = "controlled controlled NOT gate": $(q_0, q_1, q_2) \mapsto (q_0 \oplus q_1q_2, q_1, q_2).$ Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃, *a*₂₄, *a*₂₅, *a*₂₆, *a*₂₇, *a*₂₈, *a*₂₉, *a*₃₀, *a*₃₁) \mapsto (*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₅, *a*₇, *a*₆, *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, $a_{24}, a_{25}, a_{26}, a_{27}, a_{28}, a_{29}, a_{31}, a_{30}).$

Reversible comput

Say p is a permutance of $\{0, 1, \ldots, 2^n -$

General strategy to these fast quantum to obtain index per $(a_{p(0)}, a_{p(1)}, \dots, a_{p(n)})$ $\mapsto (a_0, a_1, \dots, a_{2^n})$

7₂).

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 $a_{15},$ <u>2</u>, *a*₂₃,), *a*₃₁) 6, $a_{14},$

3, *a*₂₂,

_L, *a*₃₀).

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_2, a_3, a_4, a_5, a_7, a_6)$ is a "Toffoli gate" = "controlled controlled NOT gate": $(q_0, q_1, q_2) \mapsto (q_0 \oplus q_1q_2, q_1, q_2).$ Example with more qubits: *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃, $a_{24}, a_{25}, a_{26}, a_{27}, a_{28}, a_{29}, a_{30}, a_{31}$ \mapsto (*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₅, *a*₇, *a*₆, *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, $a_{24}, a_{25}, a_{26}, a_{27}, a_{28}, a_{29}, a_{31}, a_{30}$).

Say *p* is a permutation of $\{0, 1, \ldots, 2^n - 1\}$. General strategy to compose these fast quantum operation to obtain index permutation $(a_{p(0)}, a_{p(1)}, \ldots, a_{p(2^n-1)})$ \mapsto (*a*₀, *a*₁, ..., *a*₂*n*₋₁):

Reversible computation

 $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_2, a_3, a_4, a_5, a_7, a_6)$ is a "Toffoli gate" = "controlled controlled NOT gate": $(q_0, q_1, q_2) \mapsto (q_0 \oplus q_1q_2, q_1, q_2).$ Example with more qubits: (*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₅, *a*₆, *a*₇, *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₄, *a*₁₅, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃, $a_{24}, a_{25}, a_{26}, a_{27}, a_{28}, a_{29}, a_{30}, a_{31}$) \mapsto (*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₅, *a*₇, *a*₆, *a*₈, *a*₉, *a*₁₀, *a*₁₁, *a*₁₂, *a*₁₃, *a*₁₅, *a*₁₄, *a*₁₆, *a*₁₇, *a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, $a_{24}, a_{25}, a_{26}, a_{27}, a_{28}, a_{29}, a_{31}, a_{30}$).

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General strategy to compose these fast quantum operations to obtain index permutation $(a_{p(0)}, a_{p(1)}, \ldots, a_{p(2^n-1)})$ \mapsto (*a*₀, *a*₁, ..., *a*₂*n*₋₁):

1. Build a traditional circuit to compute $j \mapsto p(j)$ using NOT/XOR/AND gates.

2. Convert into reversible gates:

e.g., convert AND into Toffoli.

 $a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ a₂, a₃, a₄, a₅, a₇, a₆) ffoli gate" = led controlled NOT gate": $(q_2)\mapsto (q_0\oplus q_1q_2,q_1,q_2).$

e with more qubits:

*a*₂, *a*₃, *a*₄, *a*₅, *a*₆, *a*₇,

10, *a*11, *a*12, *a*13, *a*14, *a*15,

*a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₂, *a*₂₃,

 $a_{26}, a_{27}, a_{28}, a_{29}, a_{30}, a_{31})$

1, *a*₂, *a*₃, *a*₄, *a*₅, *a*₇, *a*₆,

10, *a*11, *a*12, *a*13, *a*15, *a*14,

*a*₁₈, *a*₁₉, *a*₂₀, *a*₂₁, *a*₂₃, *a*₂₂, $a_{26}, a_{27}, a_{28}, a_{29}, a_{31}, a_{30}).$ Reversible computation

Say *p* is a permutation of $\{0, 1, \ldots, 2^n - 1\}$.

General strategy to compose these fast quantum operations to obtain index permutation $(a_{p(0)}, a_{p(1)}, \ldots, a_{p(2^n-1)})$ \mapsto (*a*₀, *a*₁, ..., *a*₂*n*₋₁):

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Example (a_0, a_1, a_1) (a_7, a_0, a_0) permuta 1. Build to comp q_0 $q_0 \oplus 1$

 $a_5, a_6, a_7) \mapsto a_5, a_7, a_6$

lled NOT gate": $\oplus q_1q_2, q_1, q_2$).

e qubits:

a5, **a**6, **a**7,

, *a*₁₃, *a*₁₄, *a*₁₅,

₂₀, *a*₂₁, *a*₂₂, *a*₂₃,

₂₈, *a*₂₉, *a*₃₀, *a*₃₁)

14, *a*5, *a*7, *a*6,

, *a*₁₃, *a*₁₅, *a*₁₄,

20, *a*₂₁, *a*₂₃, *a*₂₂,

₂₈, *a*₂₉, *a*₃₁, *a*₃₀).

Reversible computation

Say p is a permutation of $\{0, 1, \ldots, 2^n - 1\}$.

General strategy to compose these fast quantum operations to obtain index permutation $(a_{p(0)}, a_{p(1)}, \dots, a_{p(2^n-1)})$ $\mapsto (a_0, a_1, \dots, a_{2^n-1})$:

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 e.g., convert AND into Toffoli.


gate": $_{1}, q_{2}).$

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<u>2</u>, *a*₂₃,

), *a*₃₁)

6,

*a*₁₄,

3, *a*₂₂,

_L, a₃₀).

Reversible computation

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1. Build a traditional circuit to compute $j \mapsto p(j)$ using NOT/XOR/AND gates.

2. Convert into reversible gates: e.g., convert AND into Toffoli.



Example: Let's compute

- $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7)$
- $(a_7, a_0, a_1, a_2, a_3, a_4, a_5, a_6);$
- permutation $q \mapsto q+1$ mod
- 1. Build a traditional circuit to compute $q \mapsto q + 1 \mod q$

Reversible computation

Say *p* is a permutation of $\{0, 1, \ldots, 2^n - 1\}$.

General strategy to compose these fast quantum operations to obtain index permutation $(a_{p(0)}, a_{p(1)}, \ldots, a_{p(2^n-1)})$ \mapsto (*a*₀, *a*₁, ..., *a*₂*n*₋₁):

1. Build a traditional circuit to compute $j \mapsto p(j)$ using NOT/XOR/AND gates.

2. Convert into reversible gates: e.g., convert AND into Toffoli.

Example: Let's compute $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_7, a_0, a_1, a_2, a_3, a_4, a_5, a_6);$ permutation $q \mapsto q + 1 \mod 8$. 1. Build a traditional circuit to compute $q \mapsto q + 1 \mod 8$. q_0 q_1



q_2

le computation

- a permutation ..., $2^n - 1$ }.
- strategy to compose st quantum operations n index permutation $p(1), \ldots, a_{p(2^n-1)})$
- $a_1, \ldots, a_{2^n-1})$:
- a traditional circuit ute $j \mapsto p(j)$ OT/XOR/AND gates.
- ert into reversible gates: vert AND into Toffoli.

Example: Let's compute



2. Conv Toffoli f (a_0, a_1, a_1) (a_0, a_1, a_1)

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o compose n operations rmutation $p(2^n-1)$

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AND gates.

versible gates: into Toffoli. Example: Let's compute $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_7, a_0, a_1, a_2, a_3, a_4, a_5, a_6);$ permutation $q \mapsto q + 1 \mod 8$.

1. Build a traditional circuit to compute $q \mapsto q + 1 \mod 8$.



2. Convert into re Toffoli for $q_2 \leftarrow q_2$ $(a_0, a_1, a_2, a_3, a_4, a_4)$ $(a_0, a_1, a_2, a_7, a_4, a_4)$

1. Build a traditional circuit to compute $q \mapsto q + 1 \mod 8$.



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2. Convert into reversible ga

Toffoli for $q_2 \leftarrow q_2 \oplus q_1 q_0$:

(*a*₀, *a*₁, *a*₂, *a*₃, *a*₄, *a*₅, *a*₆, *a*₇)

 $(a_0, a_1, a_2, a_7, a_4, a_5, a_6, a_3).$

1. Build a traditional circuit to compute $q \mapsto q + 1 \mod 8$.



2. Convert into reversible gates.

Toffoli for $q_2 \leftarrow q_2 \oplus q_1 q_0$: $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ (*a*₀, *a*₁, *a*₂, *a*₇, *a*₄, *a*₅, *a*₆, *a*₃).

1. Build a traditional circuit to compute $q \mapsto q + 1 \mod 8$.



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2. Convert into reversible gates.
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Toffoli for $q_2 \leftarrow q_2 \oplus q_1 q_0$: $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_2, a_7, a_4, a_5, a_6, a_3).$ $(a_0, a_1, a_2, a_7, a_4, a_5, a_6, a_3) \mapsto$

Controlled NOT for $q_1 \leftarrow q_1 \oplus q_0$:

- $(a_0, a_7, a_2, a_1, a_4, a_3, a_6, a_5).$

1. Build a traditional circuit to compute $q \mapsto q + 1 \mod 8$.



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2. Convert into reversible gates.
Toffoli for q_2 \leftarrow q_2 \oplus q_1 q_0:
(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto
(a_0, a_1, a_2, a_7, a_4, a_5, a_6, a_3).
(a_0, a_1, a_2, a_7, a_4, a_5, a_6, a_3) \mapsto
(a_0, a_7, a_2, a_1, a_4, a_3, a_6, a_5).
NOT for q_0 \leftarrow q_0 \oplus 1:
(a_0, a_7, a_2, a_1, a_4, a_3, a_6, a_5) \mapsto
(a_7, a_0, a_1, a_2, a_3, a_4, a_5, a_6).
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- Controlled NOT for $q_1 \leftarrow q_1 \oplus q_0$:

: Let's compute $a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ a₁, a₂, a₃, a₄, a₅, a₆); tion $q \mapsto q + 1 \mod 8$.

a traditional circuit ute $q \mapsto q + 1 \mod 8$.



2. Convert into reversible gates. Toffoli for $q_2 \leftarrow q_2 \oplus q_1 q_0$: $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_2, a_7, a_4, a_5, a_6, a_3).$ Controlled NOT for $q_1 \leftarrow q_1 \oplus q_0$: $(a_0, a_1, a_2, a_7, a_4, a_5, a_6, a_3) \mapsto$ $(a_0, a_7, a_2, a_1, a_4, a_3, a_6, a_5).$ NOT for $q_0 \leftarrow q_0 \oplus 1$: $(a_0, a_7, a_2, a_1, a_4, a_3, a_6, a_5) \mapsto$ $(a_7, a_0, a_1, a_2, a_3, a_4, a_5, a_6).$

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 $a_5, a_6, a_7)\mapsto a_4, a_5, a_6); <math>q+1 \mod 8.$

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2. Convert into reversible gates. Toffoli for $q_2 \leftarrow q_2 \oplus q_1 q_0$: $(a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $(a_0, a_1, a_2, a_7, a_4, a_5, a_6, a_3).$ Controlled NOT for $q_1 \leftarrow q_1 \oplus q_0$: $(a_0, a_1, a_2, a_7, a_4, a_5, a_6, a_3) \mapsto$ $(a_0, a_7, a_2, a_1, a_4, a_3, a_6, a_5).$ NOT for $q_0 \leftarrow q_0 \oplus 1$: $(a_0, a_7, a_2, a_1, a_4, a_3, a_6, a_5) \mapsto$ $(a_7, a_0, a_1, a_2, a_3, a_4, a_5, a_6).$

This permutation was deceptively ea It didn't need mar For large *n*, most need many operat Really want *fast* c

18. 8. 2 $\exists C_1$ 2. Convert into reversible gates.

Toffoli for $q_2 \leftarrow q_2 \oplus q_1 q_0$: ($a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7$) \mapsto ($a_0, a_1, a_2, a_7, a_4, a_5, a_6, a_3$).

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This permutation example was deceptively easy.

- It didn't need many operation
- For large n, most permutation
- need many operations \Rightarrow slo
- Really want *fast* circuits.

2. Convert into reversible gates.

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For large *n*, most permutations *p* need many operations \Rightarrow slow. Really want *fast* circuits.

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This permutation example was deceptively easy. It didn't need many operations. For large *n*, most permutations *p* need many operations \Rightarrow slow. Really want *fast* circuits. Also, it didn't need extra storage: circuit operated "in place" after computation $c_1 \leftarrow q_1 q_0$ was merged into $q_2 \leftarrow q_2 \oplus c_1$.

- Typical circuits aren't in-place.

ert into reversible gates.

or $q_2 \leftarrow q_2 \oplus q_1 q_0$: $a_2, a_3, a_4, a_5, a_6, a_7) \mapsto$ $a_2, a_7, a_4, a_5, a_6, a_3).$

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Typical circuits aren't in-place.

Start fro inputs b $b_{i+1} = 1$ $b_{i+2} = 1$ $b_T = 1 \in$

specified

versible gates.

 $p_2 \oplus q_1 q_0$: $p_5, a_6, a_7) \mapsto p_5, a_6, a_3).$

or $q_1 \leftarrow q_1 \oplus q_0$: $a_5, a_6, a_3) \mapsto a_3, a_6, a_5$.

 $\oplus 1$: $a_3, a_6, a_5) \mapsto a_4, a_5, a_6).$ This permutation example was deceptively easy.

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Typical circuits aren't in-place.

Start from any cire inputs $b_1, b_2, \ldots, b_{i+1} = 1 \oplus b_{f(i+1)}$ $b_{i+2} = 1 \oplus b_{f(i+2)}$ \ldots $b_T = 1 \oplus b_{f(T)} b_{g(t)}$

specified outputs.

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 $1 \oplus q_0$:

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Typical circuits aren't in-place.

Start from any circuit: inputs $b_1, b_2, ..., b_i$; $b_{i+1} = 1 \oplus b_{f(i+1)} b_{g(i+1)};$ $b_{i+2} = 1 \oplus b_{f(i+2)} b_{g(i+2)};$ $b_T = 1 \oplus b_{f(T)} b_{g(T)};$ specified outputs.

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Typical circuits aren't in-place.

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Start from any circuit:
inputs b_1, b_2, ..., b_i;
b_{i+1} = 1 \oplus b_{f(i+1)} b_{g(i+1)};
b_{i+2} = 1 \oplus b_{f(i+2)} b_{g(i+2)};
. . .
b_T = 1 \oplus b_{f(T)} b_{g(T)};
specified outputs.
Reversible but dirty:
inputs b_1, b_2, \ldots, b_T;
b_{i+1} \leftarrow 1 \oplus b_{i+1} \oplus b_{f(i+1)} b_{g(i+1)};
b_{i+2} \leftarrow 1 \oplus b_{i+2} \oplus b_{f(i+2)} b_{g(i+2)};
```

 $b_T \leftarrow 1 \oplus b_T \oplus b_{f(T)} b_{g(T)}$. Same outputs if all of b_{i+1}, \ldots, b_T started as 0.

mutation example eptively easy.

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didn't need extra storage: perated "in place" after ation $c_1 \leftarrow q_1 q_0$ was into $q_2 \leftarrow q_2 \oplus c_1$.

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Start from any circuit: inputs $b_1, b_2, ..., b_i$; $b_{i+1} = 1 \oplus b_{f(i+1)} b_{g(i+1)};$ $b_{i+2} = 1 \oplus b_{f(i+2)} b_{g(i+2)};$ $b_T = 1 \oplus b_{f(T)} b_{g(T)};$ specified outputs. Reversible but dirty: inputs b_1, b_2, \ldots, b_T ; $b_{i+1} \leftarrow 1 \oplus b_{i+1} \oplus b_{f(i+1)} b_{g(i+1)};$ $b_{i+2} \leftarrow 1 \oplus b_{i+2} \oplus b_{f(i+2)} b_{g(i+2)};$ $b_T \leftarrow 1 \oplus b_T \oplus b_{f(T)} b_{g(T)}$. Same outputs if all of b_{i+1}, \ldots, b_T started as 0.

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en't in-place.

Start from any circuit: inputs $b_1, b_2, ..., b_i$; $b_{i+1} = 1 \oplus b_{f(i+1)} b_{g(i+1)};$ $b_{i+2} = 1 \oplus b_{f(i+2)} b_{g(i+2)};$ $b_T = 1 \oplus b_{f(T)} b_{g(T)};$ specified outputs. Reversible but dirty: inputs $b_1, b_2, ..., b_T$; $b_{i+1} \leftarrow 1 \oplus b_{i+1} \oplus b_{f(i+1)} b_{g(i+1)};$ $b_{i+2} \leftarrow 1 \oplus b_{i+2} \oplus b_{f(i+2)} b_{g(i+2)};$ $b_T \leftarrow 1 \oplus b_T \oplus b_{f(T)} b_{g(T)}$. Same outputs if all of b_{i+1}, \ldots, b_T started as 0.

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- Reversible and clean: after finishing dirty compute set non-outputs back to 0, by repeating same operation on non-outputs in reverse or
- Original computation:
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Clean reversible computation: (inputs, zeros, zeros) \mapsto (inputs, zeros, outputs). Given fast circuit for and fast circuit for build fast reversibl $(x, zeros) \mapsto (p(x))$ Reversible and clean: after finishing dirty computation, set non-outputs back to 0, by repeating same operations on non-outputs in reverse order.

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Given fast circuit for p and fast circuit for p^{-1} , build fast reversible circuit for $(x, \text{zeros}) \mapsto (p(x), \text{zeros}).$

Reversible and clean:

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Given fast circuit for p and fast circuit for p^{-1} , build fast reversible circuit for $(x, \text{zeros}) \mapsto (p(x), \text{zeros}).$ Replace reversible bit operations with Toffoli gates etc. permuting $\mathbf{C}^{2^{n+z}} \rightarrow \mathbf{C}^{2^{n+z}}$.

Permutation on first 2^n entries is $(a_{p(0)}, a_{p(1)}, \ldots, a_{p(2^n-1)})$ \mapsto (*a*₀, *a*₁, ..., *a*₂*n*₋₁).

Typically prepare vectors supported on first 2^n entries so don't care how permutation acts on last $2^{n+z} - 2^n$ entries.

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"Hadamard":

Fast quantum operations, pa

$(a_0, a_1) \mapsto (a_0 + a_1, a_0 - a_1)$

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Fast quantum operations, part 2

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Fast quantum operations, part 2

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Fast quantum operations, part 2

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"Hadamard":
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Same for qubit 1:
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Qubit 0 and then qubit 1:
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(a_0 + a_1, a_0 - a_1, a_2 + a_3, a_2 - a_3) \mapsto
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 $(a_0 + a_2, a_1 + a_3, a_0 - a_2, a_1 - a_3).$

 $(a_0 + a_1 + a_2 + a_3, a_0 - a_1 + a_2 - a_3,$

 $a_0 + a_1 - a_2 - a_3, a_0 - a_1 - a_2 + a_3).$

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Fast quantum operations, part 2 "Hadamard": $(a_0, a_1) \mapsto (a_0 + a_1, a_0 - a_1).$ $(a_0, a_1, a_2, a_3) \mapsto$ $(a_0 + a_1, a_0 - a_1, a_2 + a_3, a_2 - a_3).$ Same for qubit 1: $(a_0, a_1, a_2, a_3) \mapsto$ $(a_0 + a_2, a_1 + a_3, a_0 - a_2, a_1 - a_3).$ Qubit 0 and then qubit 1: $(a_0, a_1, a_2, a_3) \mapsto$ $(a_0 + a_1, a_0 - a_1, a_2 + a_3, a_2 - a_3) \mapsto$ $(a_0 + a_1 + a_2 + a_3, a_0 - a_1 + a_2 - a_3,$ $a_0 + a_1 - a_2 - a_3, a_0 - a_1 - a_2 + a_3).$

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Repeat *n* times: e.g., $(1, 0, 0, ..., 0) \mapsto (1, 1, 1, ...)$

- Measuring (1, 0, 0, . . . , 0) always produces 0.
- Measuring (1, 1, 1, ..., 1)can produce any output: $Pr[output = q] = 1/2^{n}.$

Fast quantum operations, part 2

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antum operations, part 2 ard": \mapsto $(a_0 + a_1, a_0 - a_1).$ $a_2, a_3) \mapsto$ $, a_0 - a_1, a_2 + a_3, a_2 - a_3).$ r qubit 1: $a_2, a_3) \mapsto$ $, a_1 + a_3, a_0 - a_2, a_1 - a_3).$ and then qubit 1: $a_2, a_3) \mapsto$ $a_0 - a_1, a_2 + a_3, a_2 - a_3) \mapsto$ $+a_2+a_3$, $a_0-a_1+a_2-a_3$, $-a_2 - a_3, a_0 - a_1 - a_2 + a_3).$

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Assume: satisfies for every Can we given a

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$$a_2 + a_3, a_2 - a_3) \mapsto a_0 - a_1 + a_2 - a_3, a_0 - a_1 - a_2 + a_3).$$

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Simon's algorithm

Assume: nonzero satisfies f(x) = f(x)for every $x \in \{0, 1\}$ Can we find this p given a fast circuit

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 $a_3) \mapsto$ $a_2 - a_3$, $a_2 + a_3).$

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Repeat *n* times: e.g.,

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Assume: nonzero $s \in \{0, 1\}$ satisfies $f(x) = f(x \oplus s)$ for every $x \in \{0, 1\}^n$. Can we find this period s, given a fast circuit for f?

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Measuring (1, 1, 1, ..., 1)can produce any output: $\Pr[\text{output} = q] = 1/2^{n}$.

Aside from "normalization" (irrelevant to measurement), have Hadamard = Hadamard⁻¹, so easily work backwards from "uniform superposition" (1, 1, 1, ..., 1) to "pure state" $(1, 0, 0, \ldots, 0).$

Simon's algorithm

Assume: nonzero $s \in \{0, 1\}^n$ satisfies $f(x) = f(x \oplus s)$ for every $x \in \{0, 1\}^n$. Can we find this period s, given a fast circuit for f? We don't have enough data

if f has many periods.

Assume: only periods are 0, s.

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- Simon's algorithm
- is much, much, much faster
- Say f maps n bits to m bits using z "ancilla" bits for reversibility.
- Prepare n + m + z qubits in pure zero state: vector (1, 0, 0, ...).
- Use *n*-fold Hadamard
- to move first *n* qubits
- into uniform superposition:
- $(1, 1, 1, \ldots, 1, 0, 0, \ldots)$
- with 2^n entries 1, others 0.

<u>Simon's algorithm</u>

Assume: nonzero $s \in \{0, 1\}^n$ satisfies $f(x) = f(x \oplus s)$ for every $x \in \{0, 1\}^n$. Can we find this period s, given a fast circuit for f?

We don't have enough data if f has many periods. Assume: only periods are 0, s.

Traditional solution: Compute f for many inputs, sort, analyze collisions. Success probability is very low until #inputs approaches $2^{n/2}$.

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Say f maps n bits to m bits, using z "ancilla" bits for reversibility.

Prepare n + m + z qubits in pure zero state: vector (1, 0, 0, ...).

Use *n*-fold Hadamard to move first *n* qubits into uniform superposition: $(1, 1, 1, \ldots, 1, 0, 0, \ldots)$ with 2^n entries 1. others 0.

algorithm

nonzero $s \in \{0, 1\}^n$ $f(x) = f(x \oplus s)$ $x \in \{0, 1\}^n$. find this period s, fast circuit for f?

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Note symmetry be 1 at (q, f(q), 0) as

1 at $(q \oplus s, f(q), 0)$

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Prepare n + m + z qubits in pure zero state: vector (1, 0, 0, ...).

Use *n*-fold Hadamard to move first *n* qubits into uniform superposition: $(1, 1, 1, \ldots, 1, 0, 0, \ldots)$ with 2^n entries 1, others 0.

Apply fast vector permutation for reversible f computation 1 in position (q, 0, 0)moves to position (q, f(q), 0)Note symmetry between 1 at (q, f(q), 0) and 1 at $(q \oplus s, f(q), 0)$. Apply *n*-fold Hadamard.

Repeat n + 10 times. Use Gaussian elimination to (probably) find s.

- Measure. By symmetry,
- output is orthogonal to s.

Simon's algorithm is much, much, much faster.

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Prepare n + m + z qubits in pure zero state: vector (1, 0, 0, ...).

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algorithm much, much faster.

aps *n* bits to *m* bits, "ancilla" bits sibility.

n + m + z qubits zero state: 1,0,0,...).

Id Hadamard first *n* qubits form superposition:

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Grover's

Assume: has f(s)

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- compute
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- Success until #i
- Grover's
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...) others 0. Apply fast vector permutation for reversible f computation: 1 in position (q, 0, 0)moves to position (q, f(q), 0).

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Grover's algorithm

Assume: unique s has f(s) = 0.

Traditional algorith compute *f* for man hope to find output Success probability until #inputs appr

Grover's algorithm reversible computa Typically: reversib is small enough th easily beats traditi Apply fast vector permutation for reversible *f* computation: 1 in position (q, 0, 0)moves to position (q, f(q), 0).

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Apply *n*-fold Hadamard.

Measure. By symmetry, output is orthogonal to s.

Repeat n + 10 times. Use Gaussian elimination to (probably) find s.

has f(s) = 0.

Grover's algorithm

- Assume: unique $s \in \{0, 1\}^n$
- Traditional algorithm to find compute f for many inputs,
- hope to find output 0.
- Success probability is very lo
- until #inputs approaches 2^n
- Grover's algorithm takes onl
- reversible computations of f
- Typically: reversibility overh
- is small enough that this
- easily beats traditional algor

Apply fast vector permutation for reversible *f* computation: 1 in position (q, 0, 0)moves to position (q, f(q), 0).

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Apply *n*-fold Hadamard.

Measure. By symmetry, output is orthogonal to s.

Repeat n + 10 times. Use Gaussian elimination to (probably) find s.

Grover's algorithm

Assume: unique $s \in \{0, 1\}^n$ has f(s) = 0.

Traditional algorithm to find s: compute f for many inputs, hope to find output 0. Success probability is very low until #inputs approaches 2^n .

Grover's algorithm takes only $2^{n/2}$ reversible computations of f. Typically: reversibility overhead is small enough that this easily beats traditional algorithm.

- st vector permutation sible f computation: ition (q, 0, 0)o position (q, f(q), 0).
- mmetry between f(q), 0) and $\oplus s, f(q), 0$.
- fold Hadamard.
- By symmetry, sorthogonal to s.
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Start fro over all Step 1: $b_q = -a$ $b_q = a_q$ This is f Step 2: Negate . This is a Repeat s about 0. Measure With hig permutation mputation: (q, f(q), 0).

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Grover's algorithm

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Grover's algorithm takes only $2^{n/2}$ reversible computations of f. Typically: reversibility overhead is small enough that this easily beats traditional algorithm. Start from uniform over all *n*-bit string

- Step 1: Set $a \leftarrow k$
- $b_q = -a_q$ if $f(q) = b_q = a_q$ otherwise This is fast.
- Step 2: "Grover d
- Negate a around i
- This is also fast.
- Repeat steps 1 and about $0.58 \cdot 2^{0.5n}$
- Measure the *n* qui With high probabi

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Start from uniform superpos over all *n*-bit strings *q*.

Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

Step 2: "Grover diffusion". Negate *a* around its average This is also fast.

Repeat steps 1 and 2

about $0.58 \cdot 2^{0.5n}$ times.

Measure the *n* qubits.

With high probability this fi

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Repeat steps 1 and 2 about $0.58 \cdot 2^{0.5n}$ times.

Measure the *n* qubits. With high probability this finds s.

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unique $s \in \{0, 1\}^n$ = 0.

nal algorithm to find s: e f for many inputs, find output 0. probability is very low nputs approaches 2^n .

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Start from uniform superposition over all *n*-bit strings *q*.

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This is also fast.

Repeat steps 1 and 2 about $0.58 \cdot 2^{0.5n}$ times.

Measure the *n* qubits. With high probability this finds *s*.

Graph of $q \mapsto a_q$ for an example with after 0 steps:



Start from uniform superposition
over all *n*-bit strings *q*.Graph
for an
after 0Step 1: Set
$$a \leftarrow b$$
 where
 $b_q = -a_q$ if $f(q) = 0$,
 $b_q = a_q$ otherwise.
This is fast.1.0Negate a_q otherwise.
This is fast.0.5Negate a around its average.0.0 $y 2^{n/2}$ This is also fast.Repeat steps 1 and 2
about $0.58 \cdot 2^{0.5n}$ times.-0.5ithm.Measure the *n* qubits.
With high probability this finds *s*.

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of $q\mapsto a_q$ example with n = 120 steps:

Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

Step 2: "Grover diffusion". Negate *a* around its average. This is also fast.

```
Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
```

Measure the *n* qubits. With high probability this finds s.

Graph of $q \mapsto a_q$ for an example with n = 12after 0 steps:



Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

Step 2: "Grover diffusion". Negate *a* around its average. This is also fast.

```
Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
```

Measure the *n* qubits. With high probability this finds s.

Graph of $q \mapsto a_q$ for an example with n = 12after Step 1: 1.0



Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

Step 2: "Grover diffusion". Negate *a* around its average. This is also fast.

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Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
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Measure the *n* qubits. With high probability this finds s.



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Repeat steps 1 and 2
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```
Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
```

Measure the *n* qubits. With high probability this finds s.

Graph of $q \mapsto a_q$ for an example with n = 12after $2 \times (\text{Step } 1 + \text{Step } 2)$: 1.0 0.5 0.0 -0.5 -1.0



Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

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Repeat steps 1 and 2
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```
Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
```

Measure the *n* qubits. With high probability this finds s.

Graph of $q \mapsto a_q$ for an example with n = 12after $4 \times (\text{Step } 1 + \text{Step } 2)$: 1.0 0.5 0.0 -0.5 -1.0



Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

Step 2: "Grover diffusion". Negate *a* around its average. This is also fast.

```
Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
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Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

Step 2: "Grover diffusion". Negate *a* around its average. This is also fast.

```
Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
```



Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

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```
Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
```

Measure the *n* qubits. With high probability this finds s.

Graph of $q \mapsto a_q$ for an example with n = 12after 7 \times (Step 1 + Step 2): 1.0 0.5 0.0 -0.5 -1.0



Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

Step 2: "Grover diffusion". Negate *a* around its average. This is also fast.

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Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
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Repeat steps 1 and 2
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Repeat steps 1 and 2
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```

Measure the *n* qubits. With high probability this finds s.

Graph of $q \mapsto a_q$ for an example with n = 12after $10 \times (\text{Step } 1 + \text{Step } 2)$: 1.0 0.5 0.0 -0.5 -1.0



Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

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Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
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Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
```

Measure the *n* qubits. With high probability this finds s.

Graph of $q \mapsto a_q$ for an example with n = 12after $12 \times (\text{Step } 1 + \text{Step } 2)$: 1.0 0.5 0.0 -0.5 -1.0



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Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
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Repeat steps 1 and 2
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Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
```

Measure the *n* qubits. With high probability this finds s.

Graph of $q \mapsto a_q$ for an example with n = 12after $20 \times (\text{Step } 1 + \text{Step } 2)$: 1.0 0.5 0.0 -0.5 -1.0



Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

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Repeat steps 1 and 2
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about 0.58 \cdot 2^{0.5n} times.
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Measure the *n* qubits. With high probability this finds s.



Good moment to stop, measure.

Step 1: Set $a \leftarrow b$ where $b_q = -a_q$ if f(q) = 0, $b_q = a_q$ otherwise. This is fast.

Step 2: "Grover diffusion". Negate *a* around its average. This is also fast.

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Repeat steps 1 and 2
about 0.58 \cdot 2^{0.5n} times.
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Graph of $q \mapsto a_q$ for an example with n = 12after $80 \times (\text{Step } 1 + \text{Step } 2)$: 1.0 0.5 0.0 -0.5 -1.0

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Very bad stopping point.





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- to understand evolution
- of state of Grover's algorith \Rightarrow Probability is ≈ 1
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3. Fast trapdoor simulation. Simulator (like prover) knows more than the algorithm does. Tung Chou has implemented this, found errors in two publications.